

**PARAMETRIC ANALOG SIGNAL AMPLIFICATION  
APPLIED TO NANOSCALE CMOS TECHNOLOGIES**

Joy Parillo

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Furthermore, lower power consumption is achieved at low frequency values of the programming range. Two different strategies can be used to extend the tuning range and preserve DR: switchable array of degenerating MOS resistors and parallel connection of identical transconductors switched by a digital word. At resonance, the peak gains are approximately 3 dB, 4 dB and 4. The impact of  $v_{th}$  on device scaling as shown in Figure 2. In this work, we implement a common-mode feedback architecture that was previously proposed in [2] but not yet realized. Effect of SSN on signal and power integrity on bit microcontroller : modeling and correlation. Raychowdhury, and K. The hardware is remotely controlled by a user interface. works have mostly focused on improving the efficiency of optimization tools without sacrificing the accuracy.